




**SUMMARY TEST DATA
ON AMPLIFIER**

Job No : PE307120
 Model No : OEC-30-2R6-10-15-SFF-RVG
 Serial No : PL1045

Tested By : Kevin Mason
 Temperature : +25°C
 Date : 08/20/03

TEST ITEM NO.	PARAMETERS	SPECIFIED VALUE	MEASURED VALUE	REMARKS QA/QC
1	Frequency Range:	2.5-6 GHz	Pass (See Plot)	
2	Gain Window:	28 to 32 dB	Pass (See Plot)	
3	Flatness:	± 0.75 dB Max.	± 0.63 dB	
4	Noise Figure:	8.5 dB Max.	Pass (See Plot)	
5	Pout @ 1dB Compression:	+10 dBm Min.	≥ +10 dBm	
6	Max. RF Power Out (Input Power -60 to 0dBm)	+18dBm	Pass	
7	Out of Band Harmonic Rejection (10-20GHz)	-30dBc Min.	Pass (See Harmonic Plot)	
8	VSWR:	Input : 1.75:1 Output: 1.75:1 (Max.)	Pass (See Plot)	
9	Operating Temp.	0 to +70 Deg. C	Pass	
10	DC Supply:	250mA @ +15V Max.	183mA @ +15V	

Production Manager Approval:  Date: 08/20/03

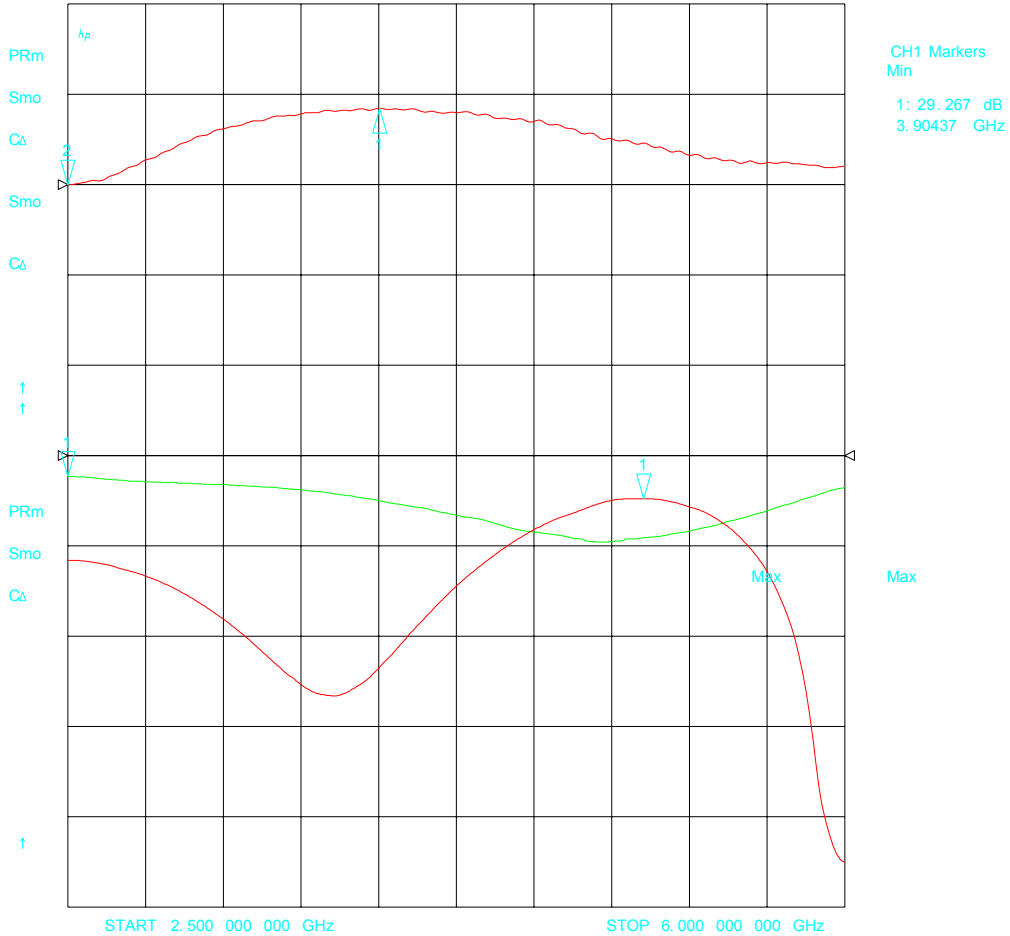
QA/QC Approval:  Date: 08/20/03



VNA Plot

20 Aug 2003 08:56:18

CH1	S21	LOG	1.5 dB/	REF 28 dB	2: 27.990 dB	2.500 000 000 GHz
CH2	S11	LOG	5 dB/	REF -11.34 dB	1: -12.504 dB	2.500 000 000 GHz
CH3	S22	LOG	5 dB/	REF -11.34 dB	1: -13.726 dB	5.094 375 000 GHz



Gain Control Option:

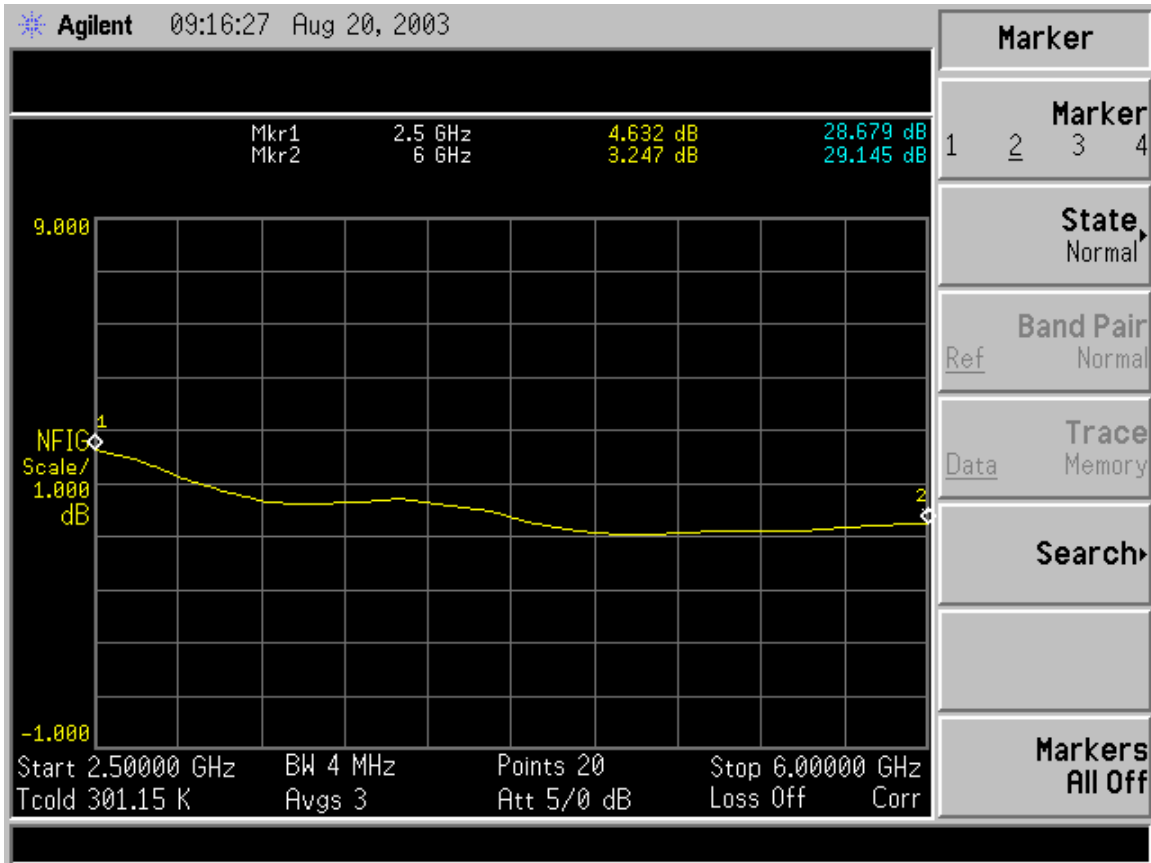
Value of resistor placed between CTL pin and ground to reduce total gain level: **(DO NOT SHORT CIRCUIT CTL PIN)**

- 260Ω = -1dB
- 147Ω = -2dB
- 101Ω = -3dB
- 78 Ω = -4dB
- 63 Ω = -5dB

7311-G Grove Road Frederick, MD 21704 USA Phone: (301)662-5019 Fax: (301)662-1731
email: sales@planarelec.com



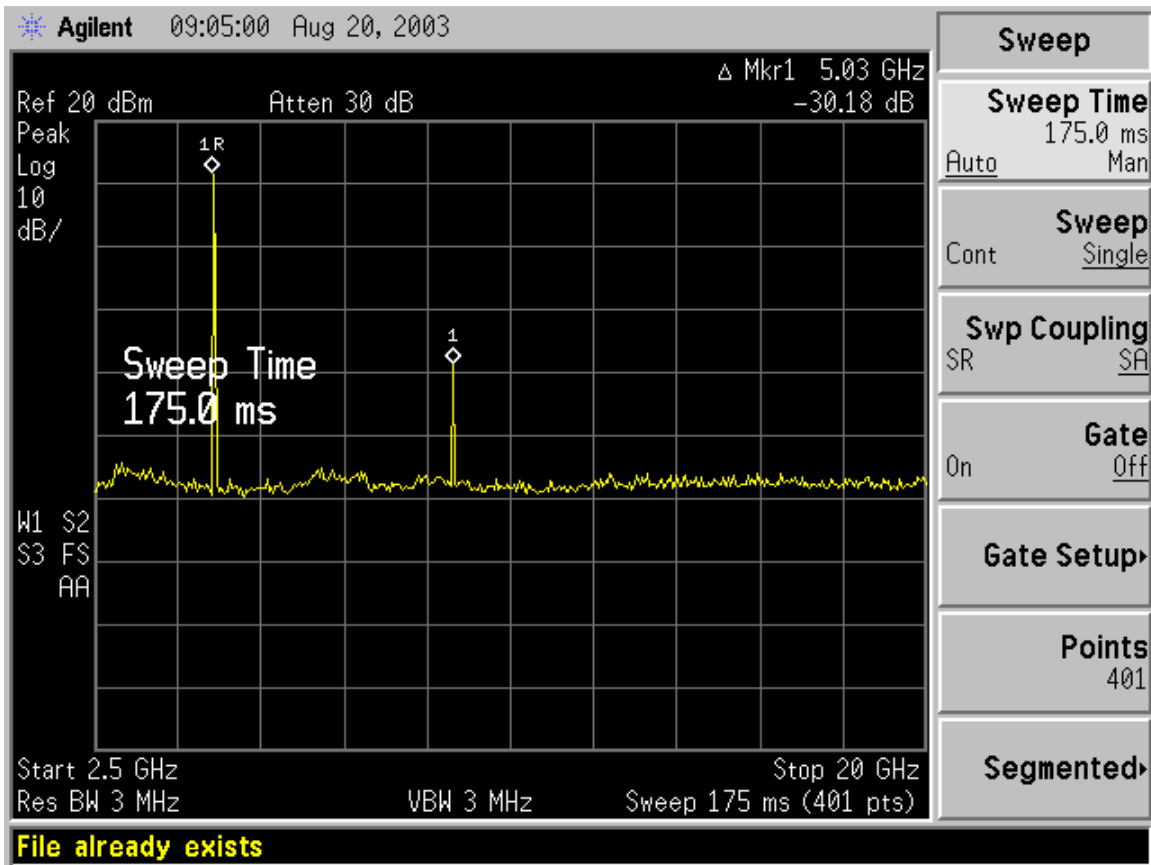
Noise Figure Plot



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Worst Case - Out of Band Harmonic Plot (10-20GHz)



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